								Ţ.			·		_	<u>- </u>			
FORM PTO-1449 (Modified)									АТ	ATTY. DOCKET NO.			IAL NO.	-	_		
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT,										C9-2000-0282US1			S. PTO				
									APPLICANT: Richard Elckemeyer et औ.						` <u> </u>		
(Use several sheets if necessary)										ING DATE:		GRO	UP:	T.			
								REFEREN	CF D	ESIGNATION			<u>. </u>		_		
								····U.S. PATE		DOCUMENTS							
EXAMINER INITIALS	!		DOCUMENT NUMBER					DATE		NAME	CLASS		SUB- CLASS	FILING DATE (IF APPRO.)			
	АА			_											_		
	AB							· <u>- · · · · · · · · · · · · · · · · · ·</u>	<u></u>	· .					_		
								FOREIGN PA	TEN	IT DOCUMENTS					_		
		DOCUMENT NUMBER					BER	DATE		COUNTRY	CLASS		SUB- CLASS	TRANSLATIO YES NO	N		
	AC																
	AD																
	AE																
				(OTH	ER.	AR	(Including Autho	r, Ti	tle, Date, Pertinent I	Pages,	etc.)					
Xh	AF			U.S. Patent Application Serial No. 09/108,160 entitled System and Method for Dispatching Groups of Instructions, filed 30 June 1998.													
X -	AG			U.S. Patent Application Serial No. 09/213,323 entitled System and Method for Permitting Out-of-Order Execution of Load Instructions, filed 16 December 1998.													
ph	АН		U.S. Patent Application Serial No. 09/213,331 entitled System and Method for Permitting Out-of-Order Execution of Load and Store Instructions, filed 16 December 1998.														
Bh	ΑI		U.S. Patent Application Serial No. 09/332,413 entitled Method and System for Restoring a Processor State Within a Data Processing System in which Instructions are Tracked in Groups, filed 14 July 1999.														
βh	AJ		Exe	U.S. Patent Application Serial No. 09/434,095 entitled System and Method for Managing the Execution of Instruction Groups Having Multiple Executable Instructions, filed 05 November 1999.													
Br	AK			U.S. Patent Application Serial No. 09/564,930 entitled Selective Flush of Shared and Other Pipelined Stages in a Multithreaded Processor, filed 04 May 2000.													
- Ar	AL			U.S. Patent Application Serial No. 09/645,08 entitled Method for Implementing a Variable-Partitioned Queue for Simultaneous Multithreaded Processors, filed 24 August 2000.													
EXAMINER	Av	~	Gul.							DATE CONSIDERED	4/1	oct			_		
	1 1						_		_			_	· · · · · · · · · · · · · · · · · · ·				

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.